

Fig.2

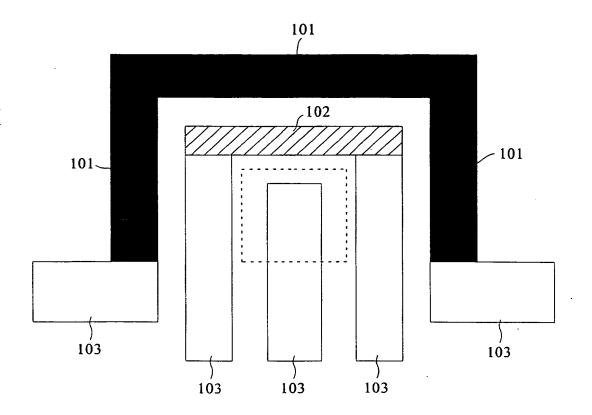
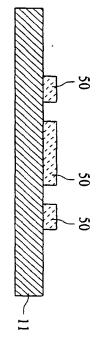
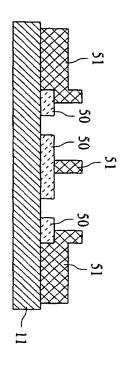


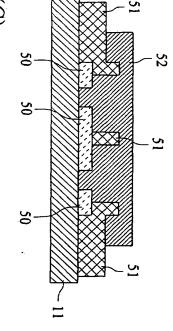
Fig.3



(A) Deposit an electric resistance



(B) Deposit a conductivity layer



(C) Coating an insulating layer

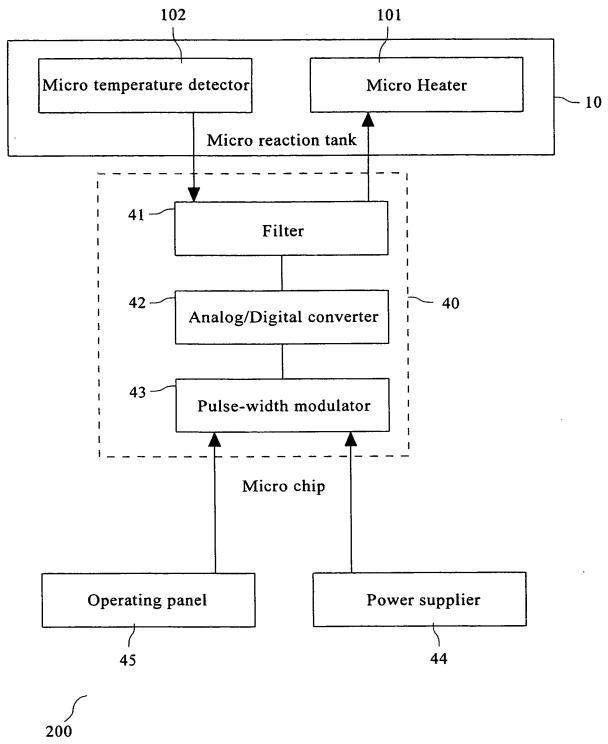


Fig.5

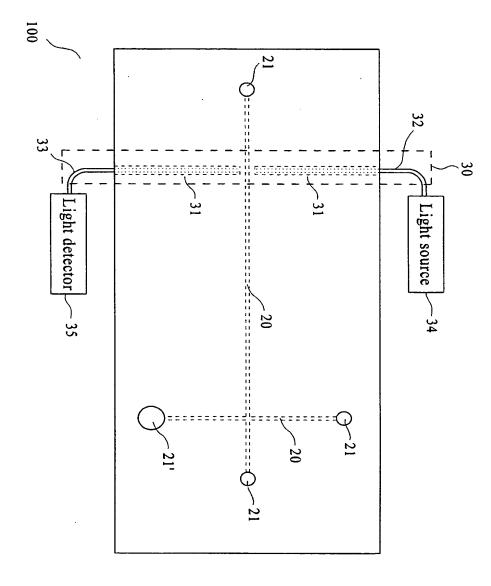


Fig.6

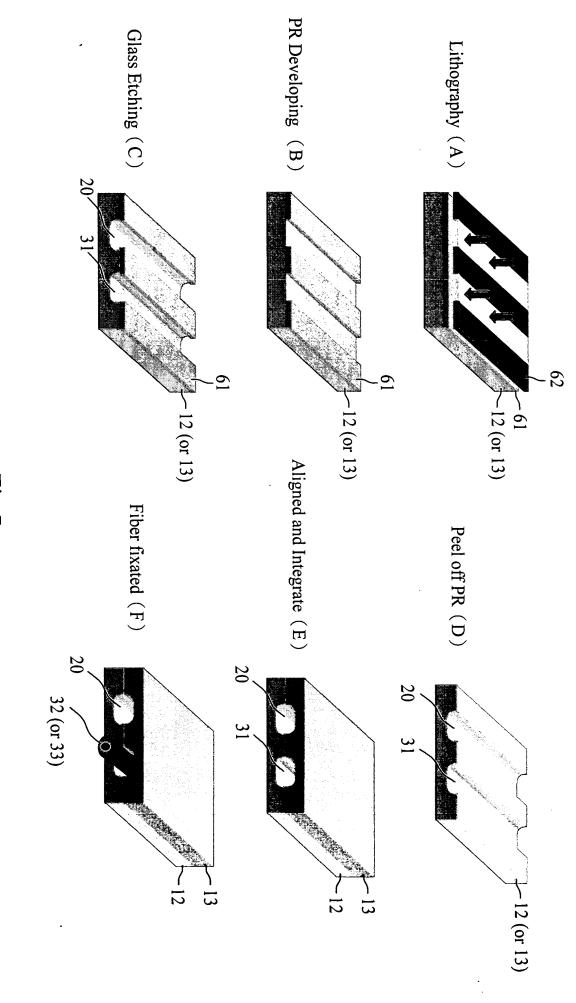
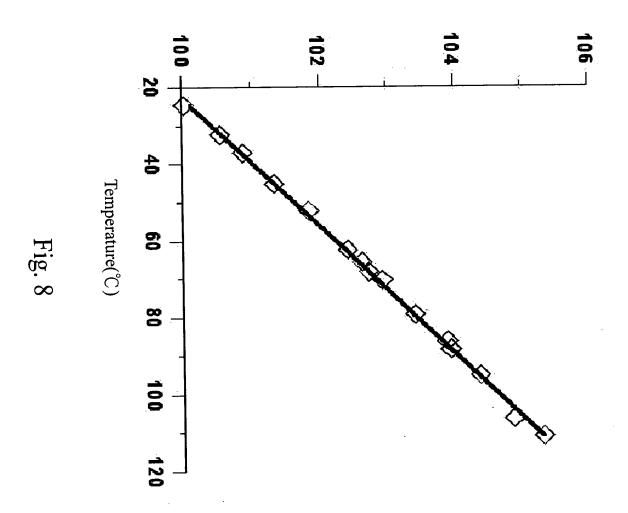
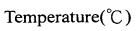
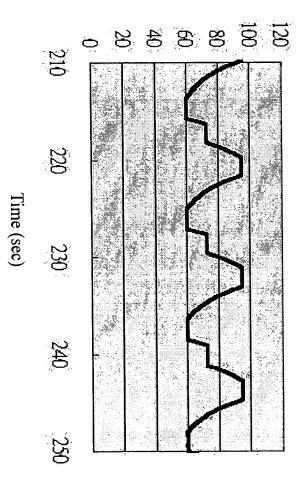


Fig.7







1g. 9

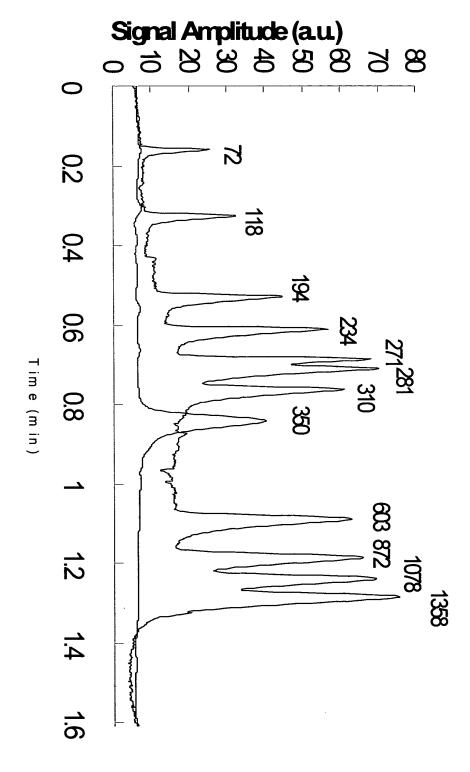


Fig. 10